

Readout Electronics Concept

- **CDS** – Correlated Double Samples is used for readout of the CCDs to achieve the required readout noise. Programmable gain receiver, dual-ramp architecture, and ADC buffer. HgCdTe compatible.
- **ADC** – 16-bit, 100 kHz equivalent conversion rate per CCD (could be a single muxed 400 kHz unit).
- **Sequencer** – Clock pattern generator supporting modes of operation: erase, expose, readout, idle.
- **Clock drivers** – Programmable amplitude and rise/fall times. Supports 4-corner or 2-corner readout.
- **Bias and power generation** – Provide switched, programmable large voltages for CCD and local power.
- **Temperature monitoring** – Local and remote.
- **DAQ and instrument control interface** – Path to data buffer memory, master timing, and configuration and control.

